

# Uncovering the Different Components of Contact Resistance to Atomically Thin Semiconductors

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Achieving good electrical contacts is one of the major challenges in realizing devices based on atomically thin 2D semiconductors. Several studies have examined this hurdle, but a universal understanding of the contact resistance (R<sub>c</sub>) and an underlying approach to its reduction are currently lacking. Here, the classical R<sub>c</sub> transmission line model description of contacts to 2D materials is experimentally examined, and a modification based on an additional lateral resistance component, namely, the junction resistance (Riun) is offered. A combination of transfer length method and contact-end measurements to characterize contacts to monolayer MoS<sub>2</sub> and separate the different R<sub>c</sub> components is used. Technology computer-aided design simulations are also used to study R<sub>c</sub> in Fermi-level pinned and unpinned contacts. This study finds that  $R_{jun}$  is the dominating component of  $R_c$  in atomically thin semiconductor devices, and is also responsible for most of the back-gate bias and temperature dependence. The experimental results help understand the underlying physics of state-of-the-art contact engineering in the context of minimizing Rjun.

## 1. Introduction

For five decades silicon-based complementary metal-oxidesemiconductor (CMOS) technologies have relied on reducing the size of transistors to increase their density on a chip, enabling a steady increase in computation capability.<sup>[1,2]</sup> As the

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lateral dimensions of field effect transistors (FETs) decrease, other dimensions must follow suit to retain electrostatic gate control.<sup>[3,4]</sup> However, reducing the thickness of transistor channels below ≈4 nm raises difficulties for conventional bulk semiconductors.<sup>[5]</sup> 2D semiconductors are therefore considered prime candidates for ultrathin channels thanks to their fewatom thickness, alongside other advantages (e.g., heterogenous integration, substrate independence, and more).<sup>[6-8]</sup> Among the main difficulties in realizing transistors, solar cells, or other electronic devices based on 2D materials is the high contact resistance  $(R_c)$ , hindering transistor output current and affecting  $V_{\rm oc}$  and  $J_{\rm sc}$  in solar cells.<sup>[8,9]</sup> Recent works have proposed reducing  $R_c$  using approaches such as varying the contact material and doping.<sup>[10–17]</sup> However, how each approach affects the measured  $R_c$  remains poorly

understood. Therefore, a universal method to understand and identify the contact resistance bottleneck is key for future contact engineering and matching the resistance reduction method to the underlying cause.

Classically, an electrical contact between a metal and semiconductor is described by its  $R_c$ , which depends on the specific contact resistivity ( $\rho_c$ ) and the sheet resistance under the contact ( $R_{sk}$ ), as seen in Figure 1a.<sup>[18]</sup> Note that  $R_{sk}$  is generally different from the sheet resistance in the channel  $(R_{\rm sh})$ , stemming from the various contact formation processes (e.g., silicidation, doping, contact deposition) that can affect the region under the contact.<sup>[19]</sup> These processes are expected to have a more severe impact on atomically thin 2D FETs where significant structural damage to monolayers due to contact metal evaporation has been reported.<sup>[20-22]</sup> The simple contact resistance model cannot capture two important phenomena that occur in contacts to 2D semiconductors: a) band bending due to the presence of a barrier (e.g., Schottky) must extend laterally into the channel,<sup>[23-25]</sup> and b) doping the access regions of 2D FETs is known to reduce their measured  $R_{c}$ .<sup>[11,12,26,27]</sup> So far, studies have explained the dependency of  $R_c$  in these phenomena by suggesting a two-path injection mechanism,<sup>[24,28,29]</sup> but there has yet to be an implementation of these effects into the contact resistance model.

In this study we present a novel experimental analysis of monolayer  $MoS_2$  transistors that uncovers a discrepancy in the contact characterization by the classical contact resistance model, which does not exist in bulk materials. We offer





**Figure 1.** Transmission line model parameters and contact-end measurements structure. a) Resistance network according to the classic contact resistance model (note that  $R_{jun}$  is not present in the classic model) along the current path from a semiconductor channel to the metal contact.  $R_{sh}$  is the semiconductor channel sheet resistance,  $R_{sk}$  is the semiconductor sheet resistance under the contacts, and  $\rho_c$  is the specific contact resistivity. The shaded area under the contact represents the difference in properties induced by the presence of the metal. The effective transfer length is noted by  $L_{tkr}$  and the physical contact length is  $L_c$ . Yellow arrows represent current flow paths. b) Schematic cross-section of the CER test structure, and the measurement probing setup. c) AFM image of the fabricated structures with equally distanced contacts (d = 350 nm) and varying contact lengths ( $L_c = 250-700$  nm). d) Raman and e) second harmonic generation mapping of the same CER structure overlaid on top of an optical microscopy image. Most of the channel is 1L with some 2L islands. The 2L channels are excluded from our analysis to prevent variations in the extracted data.

a correction to the model, modifying it to accurately detail Fermi-level pinned contacts to atomically thin 2D semiconductors. Our thorough examination of the contacts was carried out with transfer length method (TLM), contact-end resistance (CER), and four-point probe (4PP) measurements.<sup>[18]</sup> The combination of these three techniques allowed, for the first time, the separation and characterization of the vertical and lateral current injection resistances.

Our results suggest that in addition to  $\rho_{\rm c}$  and  $R_{\rm sk}$ , the intrinsic resistance components of the metal-semiconductor interface, a lateral resistance component must be introduced to explain the total contact resistance to 2D materials. Technology computer-aided design (TCAD) simulations were used to explore the barrier extension into the channel and the behavior of  $R_c$  for pinned and unpinned contacts. We adopt the junction resistance (R<sub>jun</sub>) concept proposed by Venica et al.<sup>[30]</sup> and expand it to include the lateral current injection resistance. The junction resistance accounts for the injection over or through the Schottky barrier which extends into the channel, as well as the properties of the semiconductor under the contacts and in the access region. Therefore,  $R_{iun}$  encompasses the contribution of the two factors, directly connecting access region doping and contact metal variation to R<sub>c</sub>. The implementation of R<sub>iun</sub> into the corrected contact resistance model can universally explain recent state-of-the-art data reported in the literature, unifying the physical origin of the contact resistance reduction of the various contact engineering methods.

## 2. Results and Discussion

#### 2.1. TLM and Contact-End Resistance Measurements

An important parameter of contacts with "lateral" current flow is the average length which carriers travel inside the semiconductor before they are vertically injected into the metal,<sup>[18,31]</sup> named the effective transfer length ( $L_{\rm tk}$ ) and illustrated in Figure 1a. It originates from the potential distribution under the contact which is given by<sup>[18,32]</sup>

$$V(x) = \frac{I\sqrt{R_{\rm sk}\rho_{\rm c}}}{W} \frac{\cosh\left[(L_{\rm c} - x)/L_{\rm tk}\right]}{\sinh\left(L_{\rm c}/L_{\rm tk}\right)} \tag{1}$$

where *I* is the current flowing into the contact, *W* is the contact width, and  $L_c$  is the contact length. We see that most of the carrier injection takes place near the contact-front (i.e., the channel side of the contact), and decays across the length of the contact, with a characteristic distance  $L_{tk}$ , which is given by

$$L_{\rm tk} = \sqrt{\rho_{\rm c}/R_{\rm sk}} \tag{2}$$

and is thus a product of the intrinsic parameters of the contact,  $\rho_{\rm c}$  and  $R_{\rm sk}$ . The voltage drop at the contact-front divided by the applied current results in the contact-front resistance ( $R_{\rm cf}$ ), given by

$$R_{\rm cf} = \frac{V_{x=0}}{I} = \frac{\sqrt{R_{\rm sk}\rho_{\rm c}}}{W} \coth\left(L_{\rm c}/L_{\rm tk}\right) \approx \frac{\sqrt{R_{\rm sk}\rho_{\rm c}}}{W} = \frac{\rho_{\rm c}}{L_{\rm tk}W}$$
(3)

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where the right-hand side approximation is valid for test structures where the physical contact length,  $L_c$ , is large enough compared to  $L_{tk}$  ( $L_c \gg L_{tk}$ ). The voltage drop near the contactend (i.e., the side of the contact opposite to the channel) divided by the applied current results in the contact-end resistance ( $R_{ce}$ ), given by

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$$R_{cc} = \frac{V_{x=L_c}}{I} = \frac{\sqrt{R_{sk}\rho_c}}{W} \frac{1}{\sinh\left(L_c/L_{tk}\right)} \approx \frac{2\sqrt{R_{sk}\rho_c}}{W} e^{(-L_c/L_{tk})} = \frac{2\rho_c}{L_{tk}W} e^{(-L_c/L_{tk})}$$
(4)

and can therefore be used to extract  $R_{\rm sk}$  and  $\rho_{\rm c}$ , as previously mentioned. The right-hand side approximation is again valid for  $L_{\rm c} > L_{\rm tk}$ , however only in structures where  $L_{\rm c}$  is on the same order of  $L_{\rm tk}$  ( $L_{\rm c} \sim L_{\rm tk}$ ) there is a significant and measurable voltage drop at the contact-end.

The most common method of contact characterization is the TLM measurement, which is traditionally used to separate  $R_{\rm sh}$  from  $R_{\rm c}$ .<sup>[18,19]</sup> However, this measurement does not provide information about the distribution between  $\rho_{\rm c}$  and  $R_{\rm sk}$  without the assumption that  $R_{\rm sk} = R_{\rm sh}$ .<sup>[19]</sup> which should not be the case for contacts to 2D channels, as discussed above. To circumvent this assumption, we utilize CER measurements which in conjunction with data from TLM measurements allow us to independently characterize  $R_c$ ,  $R_{\rm sh}$ ,  $R_{\rm sk}$ , and  $\rho_c$ . A schematic representation of a CER device structure is given in Figure 1b. We perform CER measurements by applying current  $I_{12}$  between terminals 1 and 2 and probing the voltage drop  $V_{23}$  between terminals 2 and 3, see Section S1 (Supporting Information) for more information. The measured contact-end resistance is then  $R_{ce} = V_{23}/I_{12}$ . For device fabrication we grew monolayer (1L) MoS<sub>2</sub> by chemical vapor deposition<sup>[33]</sup> (CVD) on SiO<sub>2</sub> (90 nm) on Si (p<sup>++</sup>), and evaporated Ni as contacts. The highly doped Si substrate serves as a global back-gate, see the Experimental Section for more information.

Figure 1c shows an atomic force microscopy (AFM) image of our CER measurement structure where the contacts are equally spaced (d = 350 nm) and vary in length ( $L_c = 250-700$  nm). Section S2 (Supporting Information) presents length measurements performed on the AFM scan images that determine dand  $L_c$ . Figure 1d,e shows Raman and second harmonic generation (SHG) mapping measurements that exhibit good agreement between them, confirming that most channels consist of 1L MoS<sub>2</sub>, with some scattered bilayer (2L) islands. SHG mapping is highly sensitive to the number and relative orientation of 2D-material-layers, thus helping with the characterization.<sup>[34-36]</sup> We expect the 2L areas to result in some variation in our extracted data, therefore these channels are excluded from our analysis. More details on the Raman and SHG spectroscopy are available in Section S3 (Supporting Information).

**Figure 2a** shows  $I_D$  versus  $V_{GS}$  measurements of a 1 µm long channel from a TLM array, performed in vacuum ambient condition (<10<sup>-4</sup> mbar) at room temperature and cryogenic conditions, presented in linear and log scale. These characteristics show typical back-gate control with  $I_{on}/I_{off} \approx 10^7$ , and 43 µA µm<sup>-1</sup> current at  $V_{GS} = 40$  V and  $V_{DS} = 1$  V, at room temperature. We measured no notable hysteresis, and the gate leakage current was negligible ( $I_G < 5$  pA). To account for the variance in electrical properties between different channels in the TLM arrays,

300



**Figure 2.** Electrical characteristics of MoS<sub>2</sub> transistors, TLM and contact-end resistance analysis. a) Linear and log-scale transfer and b) output curves of a 1  $\mu$ m long MoS<sub>2</sub> FET at 300 K (red) and 80 K (blue). The characteristics show typical electrical behavior with minimal hysteresis. Total resistance versus channel length at c) 300 K and d) 80 K. Symbols represent measured values and the solid lines are linear fits to the data. The results include data from multiple channel arrays and therefore reflect the variation in film properties due to growth and processing damage. The contact resistance extracted at 300 K is notably lower than the one extracted at 80 K and is highly dependent on the back-gate voltage. e) Measured contact-end resistance versus contact length at 300 and 80 K. A linear behavior in log-scale is observed, dashed lines are linear regressions. f) Contact-end resistance versus overdrive voltage at 300 and 80 K. R<sub>ce</sub> is independent on the back-gate voltage as was previously observed.<sup>[30]</sup>

10<sup>3</sup>

180

75

the threshold voltages ( $V_{\rm th}$ ) were extracted from the linear regime of the  $I_{\rm D}-V_{\rm GS}$  plot for each channel (not shown). All the following measurements were then performed for the same back-gate overdrive voltage ( $V_{\rm GS} - V_{\rm th}$ ).

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 $I_{\rm D}$  versus  $V_{\rm DS}$  measurements of the same channel are presented in Figure 2b, showing Ohmic characteristics at 300 K and a slight deviation at 80 K. Figure 2c,d presents the total resistance  $(R_{tot})$  versus channel length (d) for FETs from TLM arrays at 300 and at 80 K. The TLM structure and I-V curves used to extract the total channel resistances are included in Section S4 (Supporting Information). Note that due to low lithography yield these results include channel resistances from multiple TLM arrays from different parts of the sample, therefore the large spread in the measured data reflects the processing and property variations in different areas of the CVD-grown MoS<sub>2</sub>. Extracted from the slope and y-intercept, respectively,  $R_{\rm sh}$  and  $R_{\rm c}$  show dependency on the back-gate voltage  $V_{\rm GS}$ , as typically observed in these structures.<sup>[10,12]</sup> The extracted  $R_{\rm sh}$  is lower at 80 K due to the increase in carrier mobility, also noticeable from the linear regime slopes of the  $I_{\rm D}$  versus  $V_{\rm GS}$  plots. It is noted that at 80 K the total resistance is dominated by the contact resistance  $R_c$ . The large spread seen in Figure 2d can thus be attributed to the variation in contact quality for the measured channels, hindering our ability to determine the sheet resistance from TLM.

Figure 2e shows  $R_{ce}$  versus  $L_c$  where a linear behavior in log scale is observed at both room temperature and cryogenic conditions.  $V_{23}$  versus  $I_{12}$  plots are shown in Section S5 (Supporting Information), and Figure 2f displays the extracted  $R_{ce}$ versus the gate overdrive voltage  $V_{GS} - V_{th}$  for different contact lengths  $L_c$  in the same CER array. Section S6 (Supporting Information) presents  $R_{ce}$  versus  $L_c$  and  $R_{ce}$  versus gate overdrive for all contact, including the 2L MoS<sub>2</sub> excluded from the previously shown analysis. The trends remain the same, however the extracted values of  $R_{ci}$  and extraction error are higher. Interestingly,  $R_{ce}$  (and consequently  $R_{sk}$  and  $\rho_c$ ) is found to be independent on  $V_{GS}$ , in striking contrast to the  $\approx 3 \times$  decrease in

I the behavior is attributed to Fermi-level pinning by metal-induced ame gap states (MIGS), effectively fixing the carrier concentration in monolayer MoS<sub>2</sub> under the contact and hindering the charge modulation capabilities of the back-gate.<sup>[30,37,38]</sup> Thus, the tradi-00 K tional TLM model (not accounting for the junction resistance) total cannot explain how  $R_c$  is experimentally found to be dependent TLM on  $V_{GS}$ . This forces us to conclude that a third component of the contact resistance, other than  $R_{sk}$  and  $\rho_c$ , must be intro-Secduced to settle this contradiction. It is this unique combination of TLM and CER measurements of contacts to atomically thin 2D semiconductor channels which uncovers the additional component of the contact resistance, which is not included in the classical transmission line model of contact resistance to

bulk materials.

Our CER measurements show that the back-gate voltage modulates the energy bands with respect to the Fermi level in the MoS<sub>2</sub> channel, but not under the contacts. To account for the gate dependency of  $R_c$  that is shown by TLM measurements, we adopt the concept of the pseudo-junction resistance ( $R_{jun}$ ) first proposed by Venica et al.<sup>[30]</sup> and depicted in **Figure 3**a.  $R_{jun}$  is a series-resistance component of  $R_c$  which was originally attributed to the formation of a lateral junction between the channel and the area under the contact due to a variation in the material properties caused by the metal deposition. The variation can stem from several mechanisms such as physical damage from the deposition process, bandgap variation and Fermi level pinning by the contacting metal.<sup>[21,22,37–39]</sup>

Here, we use  $R_{jun}$  to describe the injection resistance which includes the tunneling through, and thermionic emission over, the Schottky barrier (SB) between the metal and semiconductor.  $R_{jun}$  is a unique feature of atomically thin 2D materials, rooted in the lateral extension of the barrier directly from the contact into the channel, shown in Figure 3b.<sup>[23,24]</sup> Figure 3c,d shows a TCAD simulation of the potential distribution near the Ni/MoS<sub>2</sub> interface for 1L and 10L MoS<sub>2</sub>. For 1L MoS<sub>2</sub> the depletion is mainly lateral, which also dictates lateral current flow



**Figure 3.** Cartoon and TCAD simulations illustrating the junction resistance component,  $R_{jun}$ . a) Device schematics showing where the junction resistance fits between the metal contact and the 2D semiconductor channel. The intrinsic contact resistance  $R_{ci}$  is defined as the combined contribution of  $R_{sk}$  and  $\rho_c$ . b) Cartoon of the band diagram of the SB components of  $R_{jun}$ . The effect of the back-gate voltage is illustrated in red. The alignment of the SB for injection is depicted to emphasize the origin of  $R_{jun}$ . TCAD simulation showing the potential distribution for c) 1L and d) 10L MoS<sub>2</sub>/Ni contact. For atomically thin MoS<sub>2</sub> the potential decays laterally and for thicker MoS<sub>2</sub> the potential also decays vertically.



from the contact, while for 10L MoS<sub>2</sub> the depletion (and current flow) has a significant vertical component. This reiterates that  $R_{\text{jun}}$  is most relevant for atomically thin semiconductors. Technical details about the TCAD simulation are found in Section S7 (Supporting Information) which also presents an estimation of the lateral extension of the barrier at different carrier concentrations. The barrier penetrates about 10 nm into the MoS<sub>2</sub> channel for a carrier concentration of  $n = 5 \times 10^{10}$  cm<sup>-2</sup>, however this length shortens as the back-gate voltage is increased.

Although not all components of the injection resistance are necessarily serial in nature (i.e., some components of lateral and vertical injections can be thought of being parallel to each other) we choose to lump them as  $R_{jun}$  for the sake of the simplicity of the discussion. A more complicated procedure of modeling the contribution of the junction resistance could further correct the model, but the simple series resistance assumption holds well enough for our data. We note that for cases where only lateral or vertical injection are present (e.g., pure edge contacts only exhibit lateral carrier injection)  $R_{jun}$  will be solely determined by that mechanism.

The extreme thinness of atomically thin 2D semiconductors prevents the SB from expanding vertically under the contact. We thus expect the majority of tunneling through the barrier to take place directly into the channel, even for high concentration of charge carriers under the contact that may be induced by Fermi level pinning.<sup>[23,24]</sup> As the back-gate voltage increases, more electrons occupy the channel. Because the tunneling distance is inversely proportional to the carrier concentration, the lateral carrier injection becomes more probable and consequently the junction resistance decreases as the overdrive voltage increases, shown schematically in Figure 3b. We therefore conclude that  $R_{jun}$  is highly dependent on  $V_{GS}$ . To separate between the gate-dependent and independent components of the total contact resistance ( $R_{c.tot}$ , extracted from TLM) we define the intrinsic resistance under the contact  $(R_{c,i})$  similar to the classical contact-front resistance  $R_{cf}$  (i.e., the contact resistance without any lateral injection)

$$R_{\rm c-i} = R_{\rm cf} = \frac{\sqrt{R_{\rm sk}\rho_{\rm c}}}{W}$$
(5)

as illustrated in Figure 3a, and thus  $R_{c-tot}$  is comprised of  $R_{c-i}$  and  $R_{\rm jun}$ . Using this, the y-intercept of  $R_{\rm ce}$  versus  $L_{\rm c}$ , extrapolated from the range  $L_c > L_{tk}$  now yields  $R_{ce}$  ( $L_c = 0$ ) =  $2R_{c-i}$ , as shown in Figure 2e. We can also extract  $L_{tk} = 240 \pm 10$  nm at 300 K and  $270 \pm 20$  nm at 80 K, which is quite large in the context of contact scaling, as previously discussed in depth by Schulman et al.,<sup>[25]</sup> and will result in increased  $R_{c-tot}$  which will ultimately limit the performance of ultra-scaled devices. However, this transfer length can be substantially lowered with proper engineering to reduce  $\rho_{\rm c}$ . Furthermore, we can determine the contact resistivity and sheet resistance under the contact as  $\rho_{\rm c}$  = 2  $\pm$  0.2  $\times$  10^{-6}  $\Omega$  cm² at 300 K and 1.2  $\pm$  0.2  $\times$  10^{-6}  $\Omega$  cm² at 80 K,  $R_{\rm sk}$  = 3.5 ± 0.5 k $\Omega$   $\Box^{-1}$  at 300 K and 1.7 ± 0.3 k $\Omega$   $\Box^{-1}$  at 80 K. We note that this is the first time  $R_{\rm sk}$  values are reported for 1L CVD grown MoS<sub>2</sub>, and among the few results reported for any 2D materials.<sup>[40–43]</sup> This  $R_{\rm sk}$  value is low compared to the lowest reported  $R_{\rm sh}^{[12]}$  due to the high carrier concentration induced by the Fermi level pinning under the contacts.



An *incorrect* extraction of  $R_{\rm sk}$  from TLM and CER without accounting for  $R_{\rm jun}$  is presented in Section S8 (Supporting Information). When we do not consider the lateral extension of the depletion into the channel beyond the contact "front"  $(R_{\rm jun})$  we get artificially large and incorrect  $R_{\rm sk}$  values, and an unexplained  $R_{\rm sk}$  dependency on  $L_{\rm c}$ . This stems from the inclusion of the junction resistance effect in the TLM measurements, and therefore skewing the extraction of  $R_{\rm sk}$ . It is of interest to note that  $\rho_{\rm c}$  seems to be lower at 80 K, which commonly is not the case when thermionic emission dominates the carrier injection. However, considering the strong doping caused by Fermi level pinning we can see that the carrier injection is in the tunneling regime which shows a decrease in the contact resistivity at lower temperatures, as previously reported.<sup>[44,45]</sup>

#### 2.2. Four-Point Probe (4PP) Measurements

To extract  $R_{jun}$  we continued our characterization with 4PP measurements using the device structure shown in **Figure 4**a. The different contacts in this structure consist of two large-area current driving probes (1 and 4), and two small-area voltage measurement probes (2 and 3). The small size of the voltage probes and the large distance (compared to the  $\approx$ 10 nm lateral barrier extension) between them helps reducing their effect on the overall resistance (e.g., a potential drop due to the junction resistances between the voltage probes), and therefore we neglect their contribution. *I*–*V* curves measured between large (1) and small (2) terminals, presented in Figure 4b, show reduced current when charge carriers (electrons) are injected from the latter, suggesting that the current flow bottleneck is the carrier-injecting contact (source).

 $R_{\rm sk}$  and  $\rho_{\rm c}$  are determined only by the metal–semiconductor interface, thus their resistance contribution is expected to be independent on whether the electrode injects or collects current (acting as source or drain, respectively). In contrast, R<sub>jun</sub> includes the thermionic emission over the SB that is only present where carriers are injected. Therefore, we conclude that the measured asymmetry in the I-V curves suggests that  $R_{iun}$ is more prominent at the carrier-injecting contact. We note that some asymmetry may result from contact-to-contact variation caused by fabrication and variations in the MoS<sub>2</sub> CVD growth, however we generally measured larger resistances at the source side by measuring the same structure with both polarities. Section S9 (Supporting Information) details a potential measurement across the 4PP device channel showing larger potential drops at the source contact compared to the drain contact, further affirming our claim.

To extract  $R_{jun}$  we applied current  $I_{14}$  between terminals (1) and (4) and probed the voltage  $V_{23}$  between terminals (2) and (3). We started by extracting the sheet resistance expressed by

$$R_{\rm sh} = W V_{23} / I_{14} d_{23} \tag{6}$$

where  $d_{23}$  is the distance between probes (2) and (3). Then, we extracted the total contact resistance which is expressed by

$$2 R_{\rm c-tot} = W V_{14} / I_{14} - R_{\rm sh} d_{14} \tag{7}$$





**Figure 4.** Four-point probe (4PP) test structure and measurement. a) AFM topography image of the 4PP measurement structure, Ni contacts, and leads are labeled. b) *I*–V curves between terminals (1) and (2) of the 4PP structure for  $V_{GS} = 0$  to 40 V. The current is higher when the carrier injection (at the source electrode) takes place at the large terminal (1).  $R_{jun}$  and  $R_{c-i}$  versus gate overdrive voltage at c) 300 K and d) 80 K. The square symbols show  $R_{c-tot}$  obtained from TLM, and the uncertainty is represented by the error bars. The TLM values are in good agreement with  $R_{jun}$  obtained from our analysis, suggesting that  $R_{c-tot}$  is dominated by  $R_{jun}$ .

where  $V_{14}$  and  $d_{14}$  are the voltage drop and distance between contacts (1) and (4) respectively.  $R_{c-tot}$  is double-counted because the current path runs through two semiconductor-metal interfaces. Finally,  $R_{jun}$  is expressed and calculated by the following simple expression

$$R_{\rm jun} = R_{\rm c-tot} - R_{\rm c-i} \tag{8}$$

where  $R_{c-i}$  is extracted from CER measurements.

 $R_{iun}$  and  $R_{c.i}$  as a function of the overdrive voltage are plotted at 300 and 80 K in Figure 4c,d, respectively. The total contact resistance values obtained from TLM are overlain for comparison (e.g.,  $R_{c-tot} = 31 \pm 8.1 \text{ k}\Omega \text{ }\mu\text{m}$  at  $V_{GS} - V_{th} = 10 \text{ V}$  and 300 K). The high contact resistance is a result of the Fermi level pinning leading to a large SB height, and  $R_{iun}$  is not reduced by any means of heavy doping or gating the access regions near the contacts.  $R_{jun}$  exhibits a strong  $V_{GS}$  dependency, expectedly decreasing as the overdrive voltage, and consequently the channel carrier density, is increased. On the other hand,  $R_{c-i}$  was found to be gate-independent during CER measurements, echoing the effect of Fermi level pinning on the carrier concentration under the contacts. It is important to note that although some electric charge is induced under the contacts by the applied back-gate voltage through a capacitive effect, it is not enough to overcome the Fermi level pinning and therefore does not affect the band diagram under the contacts. We further discuss this point later and show TCAD simulation results to support our claim. Comparing measurements at 300 and 80 K we see that  $R_{jun}$  increases at lower temperature, in agreement with the TLM measurements, although  $R_{c-i}$  shows marginal change. This is explained by the temperature dependence of the current injection mechanism into the channel (thermionic emission and thermionic field emission<sup>[46]</sup>) or under the contact (tunneling<sup>[44,45]</sup>). Namely, reducing the temperature decreases the thermionic emission probability and the electron effective mass, therefore increasing  $R_{jun}$  while decreasing  $R_{c-i}$ .

We can now explain that for the Ni contacts considered here, the gate-dependency of  $R_{c-tot}$  is determined by  $R_{jun}$  through the electrostatic gating of the access regions, while  $R_{c-i}$  is gate-independent owing to the Fermi level pinning. Moreover, because in our devices  $R_{jun}$  results from the large SB height between Ni and MoS<sub>2</sub>, it is high compared to  $R_{c-i}$  and dominates  $R_{c-tot}$ , in good agreement with the TLM results. We reiterate that although our reported value of  $L_{tk}$  is 240 nm,  $R_{jun}$  is considered as a series resistance component of  $R_{c-tot}$  in atomically thin semiconductors due to the band bending extending laterally into the channel. Because the charge carriers must pass through the junction prior to traveling under the contact,  $R_{jun}$ is present in our devices and plays a role in  $R_{c-tot}$  regardless of the value of  $L_{tk}$ .

In contrast to our Ni contacts, the behavior of optimized contacts (e.g.,  $R_{c-tot} < 1 \text{ k}\Omega \text{ }\mu\text{m}$ ) can be universally explained as  $R_{jun}$ is reduced and  $R_{c-tot}$  is not necessarily dominated by  $R_{c-i}$  or  $R_{jun}$ .



This is achieved by either increasing the channel carrier concentration and decreasing the SB width,<sup>[12]</sup> or by de-pinning the Fermi level and decreasing the SB height.<sup>[13,47]</sup> For example, Jin et al.<sup>[48]</sup> recently explored contact gating for semi-metallic Bi contacts on WS<sub>2</sub>, where the Fermi level is unpinned under the contacts due to the lack of MIGS at the Bi-WS2 interface. We expect  $R_{c,i}$  to be gate dependent for such contacts. Moreover,  $R_{iun}$  is expected to be on the same order of magnitude as  $R_{ci}$  due to the low SB height thanks to the Bi contacts, resulting in almost no extension of the barrier into the channel.<sup>[13]</sup> They studied a dual gated WS<sub>2</sub> FET which has a global back-gate that freely modulates the carrier concentration under the contacts and in the channel region, and a local top-gate that only modulates the carrier concentration in the channel region. They report that  $R_{c-tot}$ is strongly affected by the back-gate, which is readily explained by the reduction of both  $R_{c-i}$  and  $R_{jun}$  in the case of unpinned Bi contacts. However, they also report a weaker dependency of  $R_{\text{c-tot}}$  on the top-gate voltage, which we can explain by the reduction of  $R_{iun}$ . Their observed modulation of  $R_{c-tot}$  by the top-gate is rather weak compared to the bottom-gate because of two possible reasons. First, although  $R_{c-tot}$  is determined by both  $R_{c-i}$ and  $R_{iun}$  due to the low SB height of the Bi contacts, the top-gate can only modulate  $R_{jun}$  but the bottom-gate can modulate both  $R_{iun}$  and  $R_{c-i}$ . Second, the short lateral extension of the SB into the access regions is poorly gated by the top-gate due to limitation of the device structure having top-contacts. Meaning that even if  $R_{c-tot}$  is dominated by  $R_{iun}$ , the bottom-gate has better electrostatic control over the access regions resulting in stronger modulation of  $R_{c-tot}$  compared to the top-gate.

#### 2.3. TCAD Simulations

Finally, we present TCAD simulations performed with Sentaurus simulation tool to further study the effect of Fermi level pinning under the contacts on R<sub>c-i</sub>. Because in our Ni contacts to  $\ensuremath{\mathsf{MoS}}_2$  the Fermi level is pinned, the application of back-gate voltage does not change the number of electrons in that region. Therefore, it is understandable that  $R_{c,i}$  is experimentally found to be independent on  $V_{GS}$  as it is only affected by the properties of the semiconductor under the contacts. However, if the Fermi level is not fully pinned and the electrostatic doping induced by the back-gate is strong, some dependency of  $R_{c-i}$  on  $V_{GS}$  is expected. Using TCAD simulations we can estimate the number of interface traps required to pin the Fermi level. We note that the Fermi-level pinning is not forced (e.g., by specifying a strict energy location for the Fermi level), instead the Fermi level energy location is calculated by the TCAD simulator depending on the specified trap density, charge neutrality level (CNL), and boundary conditions, similar to the Bardeen model.<sup>[49]</sup> As detailed by the model, both donorlike and acceptor-like traps are present at different energy intervals in the semiconductor bandgap at the metal interface, and the energy crossover between donor- and acceptor-like traps is defined as the CNL. If the trap concentration is high (see values below), the Fermi level will be pinned to the CNL to avoid large charge at the interface.

The device structure we used for the simulations closely resembles the work shown in previous publications.<sup>[17,28,50]</sup>

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In short, 0.6 nm thick 1L MoS<sub>2</sub> is defined on top of a SiO<sub>2</sub>/Si substrate, which replicates the fabricated 1L MoS<sub>2</sub> devices. We model a 0.3 nm van der Waals gap between the MoS<sub>2</sub> layer and the metal contacts by inserting a vacuum region ( $\varepsilon = 1$ ) under the Source and Drain. The interface traps are defined by a surface concentration with a single energy level, usually placed relative to the conduction band.<sup>[49]</sup> The simulation then solves Poisson's equation coupled with drift-diffusion equations and Fermi–Dirac statistics, and the boundary conditions we set act as the applied gate and drain voltages.

Figure 5 depicts the simulated MoS<sub>2</sub> device band diagrams with three concentrations of interface traps ranging from  $D_{\rm it} = 10^{11}$  to 5 × 10<sup>13</sup> cm<sup>-2</sup>. We defined both donor-like and acceptor-like traps and placed the CNL at 150 meV below the conduction band. The band diagrams show the Fermi level at the semiconductor edge under the contact as being unpinned, semi pinned, and completely pinned depending on the concentration of interface traps, similar to the Bardeen model prediction for the barrier height between metal and semiconductor.<sup>[49]</sup> We see that for  $D_{it} > 5 \times 10^{13} \text{ cm}^{-2}$  the Fermi level is pinned at the CNL and any applied  $V_{\rm GS}$  does not change its position. Thus, the electron concentration under the contact remains constant for this concentration of interface traps. In contrast, for  $D_{it} < 10^{11} \text{ cm}^{-2}$  no pinning is observed and the Fermi level is free to traverse the MoS<sub>2</sub> bandgap as dictated by the applied back-gate voltage. We note that the SB height in this case is still determined by the work function difference between the metal and  $MoS_2^{[49]}$  and not the by back-gate. For  $D_{it} = 5 \times 10^{12} \text{ cm}^{-2}$ an intermediate state is observed where the Fermi level is neither pinned nor unpinned. It is found close to the CNL, but the applied  $V_{GS}$  does affect its location. These observations are not specific to the MoS2/Ni interface studied here, Bardeen estimated the threshold value required for pinning to be  $D_{\rm it}$  > 10^{13}  $\rm cm^{-2},^{[49]}$  and the results of our TCAD simulations coincide with the prediction. The experimental evidence of the independency of  $R_{c-i}$  on  $V_{GS}$  (i.e., Fermi level pinning) suggests that the density of interface traps at the fabricated  $MoS_2/Ni$ interfaces is larger than  $\approx 5 \times 10^{13}$  cm<sup>-2</sup>. Moreover, we can also project that to unpin the Fermi level, a clean interface with  $D_{it}$ of less than 10<sup>11</sup> cm<sup>-2</sup> is needed.

## 3. Conclusions

In conclusion, we have conducted TLM, CER, and 4PP measurements at room temperature and cryogenic conditions to rigorously characterize the contact resistance to monolayer MoS<sub>2</sub>. The TLM extraction of  $R_{\rm sh}$  and  $R_{\rm c}$  showed that the contact resistance is strongly dependent on the back-gate voltage. In contrast, CER results indicated that the components of the contact resistance physically located at the metal–2D semiconductor interface,  $\rho_{\rm c}$  and  $R_{\rm sk}$ , are unaffected by gate bias which was attributed to Fermi level pinning under the contact resistance components,  $\rho_{\rm c}$  and  $R_{\rm sk}$  lumped as  $R_{\rm c-i}$ , a third component,  $R_{\rm jun}$ , must be introduced to explain the total contact resistance to 2D semiconductors.  $R_{\rm c-i}$  accounts for the intrinsic metal–semiconductor interface resistance.  $R_{\rm jun}$  accounts for the thermionic emission over and tunneling through the



**Figure 5.** TCAD simulated band diagrams of MoS<sub>2</sub> for a)  $D_{it} < 10^{11}$  cm<sup>-2</sup>, b)  $D_{it} = 5 \times 10^{12}$  cm<sup>-2</sup>, and c)  $D_{it} > 5 \times 10^{13}$  cm<sup>-2</sup>. The charge neutrality level placed at 150 meV below the conduction band. The shaded area highlights the MoS<sub>2</sub> under the contact, and the dashed black line represents the Fermi level.

Schottky barrier at the contact edge, which in the case of atomically thin 2D semiconductor devices extends into the channel.

We performed 4PP measurements and used a simplified model to calculate and distinguish between  $R_{iun}$  and  $R_{c-i}$ , showing that  $R_{jun}$  expresses the gate- and temperaturedependence of the total contact resistance, while  $R_{c.i}$  was found to be mostly gate-independent. Furthermore, R<sub>jun</sub> dominated the contact resistance to our devices, being at least  $5 \times$  larger than  $R_{c-i}$  for the tested gate bias range. Finally, we used TCAD simulations to study Fermi level pinning under the contacts and estimated that to achieve fully unpinned contacts, clean metal-semiconductor interfaces with  $D_{\rm it} < 10^{11} \ {\rm cm^{-2}}$  are desired.  $R_{\rm jun}$  can also help explain recent reported data of state-of-the-art contact resistance reduction methods such as oxide doping,<sup>[12]</sup> semimetallic contacts,<sup>[13]</sup> and edge contacts.<sup>[51]</sup> The first is explained by the increased carrier concentration near the contacts, thus lowering the tunneling distance and reducing the lateral injection resistance. The second is explained by the lowering of the Schottky barrier for injection through de-pinning of the Fermi level, again reducing the injection resistance. The last is explained by shunting the vertical charge transport and directly injecting charge carriers to the channel, essentially eliminating  $R_{c-i}$ . Thus, engineering contacts that take advantage of all the aforementioned methods should result in contacts with ultralow contact resistance. Therefore, by deepening the understanding of the different contact resistance components, our findings help characterize, analyze, and ultimately design better contacts to atomically thin semiconductors.

## 4. Experimental Section

MoS<sub>2</sub> Device Fabrication: MoS<sub>2</sub> was grown by a CVD process on  $SiO_2$  (90 nm)/Si (p<sup>++</sup>) substrates which also serve as global back-gate; more details on the CVD process are found in Section S10 (Supporting Information). All patterning steps were done by e-beam lithography using Raith EBPG 5200. First, 50 nm thick Ni contacts were defined and e-beam evaporated under high vacuum ( ${\approx}4 \times 10^{-8}$  Torr) followed by lift-off in acetone and cleaning in IPA. Second, MoS<sub>2</sub> was etched in RIE Plasma-Therm 790 to define the channels using 90 s O2 plasma with 20 sccm gas flow, 10 W radio frequency (RF) power, and 20 mTorr ambient pressure. Finally, Ti/Ni/Au (15/15/20 nm) probing pads and leads were evaporated, with Ti acting as an adhesion layer. The TLM structures consist of equal length contacts ( $L_c = 750$  nm) with varying spacings (d = 40-4800 nm). In the CER structure the contacts were equally spaced (d = 350 nm) and vary in length  $(L_c = 250-700 \text{ nm})$ . The 4PP structure has two large current probes spaced 2.7  $\mu$ m apart, and two voltage probes spaced 1.15 µm apart, and 330 nm from the current probes.

*Characterization*: The MoS<sub>2</sub> sample topography was first characterized using optical microscopy (Zeiss Axiotron). The Raman and second harmonic generation spectroscopy and mapping were performed with a WITec alpha300 R instrument using 532 nm laser, 1200 g mm<sup>-1</sup> grating, and 100× objective lens. The WITec Project FIVE software was used for analysis, and the Si peak position at 520 cm<sup>-1</sup> was used for intensity normalization of all spectra. Electrical characterization was carried out with a Keysight B1500 semiconductor parameter analyzer (SPA) in a Janis probe station in vacuum conditions (<10<sup>-4</sup> mbar) at room temperature (near 300 K) and at cryogenic conditions (80 K).

# Supporting Information

Supporting Information is available from the Wiley Online Library or from the author.

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# **Conflict of Interest**

The authors declare no conflict of interest.

# **Author Contributions**

E.B. and E.Y. conceived the experiments and wrote the manuscript with input from all authors. R.W.G. grew the  $MoS_2$ . E.B. fabricated the devices and performed electrical, PL, and Raman characterizations.

## **Data Availability Statement**

The data that support the findings of this study are available from the corresponding author upon reasonable request.

## Keywords

contact end resistance, contact resistance, four-point probe, junction resistance, transfer length method

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